

Jobs in Santiago: Computer Science/Electronics Engineers

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There are several open job reqs in the Synopsys R&D Center in Santiago de Chile, e.g.:

- C / C++ software developers, and
- Electronics engineers for CAE positions with experience in EDA and digital electronics design (VHDL, Verilog, SystemVerilog, FPGA, etc.).

If you are interested then please send your CV in English to the following email:
synopsys@gmx.com

"Expats" who want to stay at least two years and preferably longer are certainly welcome and will be received with open arms.

Job ad for software developers:

Do You want to write the Software used to Change our Lives?

Synopsys Software is used to design the hearts of products that change our way of doing things.

Become part of the Synopsys Chile R&D Center and join the most successful team of Chilean engineers in the software for electronics industry.

Synopsys, after 5 successful years of operation in Chile, is growing its local operations and is looking for the best professionals to develop high tech software.

Job Description:

- Employee will engage in R&D effort in the core infrastructure for the next generation of Electronic Design Automation (EDA) software.
- Responsibilities include writing functional and design specifications, designing and developing the infrastructure, integration and unit testing, and working with worldwide cross functional teams such as other R&D and QA groups.

Requirements :

- Engineering in Computer Science / Electronics or equivalent
- Software engineering background
- Experience in C/C++ (Java/C#) programming
- Software development and debugging experience in Unix/Linux environment
- 0 to 5 years of experience developing large scale software applications
- Analytical and problem solving skills
- Good teamwork skills
- Good communication skills in English

Contact Synopsys CHILE now!

Send your resume in English to
synopsys@gmx.com

www.synopsys.com